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PATENT
81788.0265

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Yoshiyuki Tanaka, et al.

Serial No: 10/786,960

Confirmation No.: 4129

Filed: February 25, 2004

For: METHOD FOR CONTROLLING NON-VOLATILE SEMICONDUCTOR MEMORY SYSTEM

STATUS REQUEST

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Art Unit: 2186

Examiner: Not assigned

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450, on	
<u>September 27, 2005</u>	
Date of Deposit	
William H. Wright, Reg. No. 36,312	
Name	9/27/05
Signature	Date

The above-referenced application was filed on February 25, 2004 and the Notice of Publication of Application was received on January 19, 2005. To date, no further papers have been received in this case.

Please send the undersigned a Status Report on this application.

Respectfully submitted,

HOGAN & HARTSON L.L.P.

By:

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Date: September 27, 2005

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